CARDEXPERT TECHNOLOGY, INC.
CIRRUS LOGIC GD542X (VER. 4.2)

Category	Video
Video Types Supported	XVGA
Video Processor	Cirrus Logic GD542X
Highest Resolution Supported	1280 x 1024
Data Bus Type	32-bit Vesa Local Bus
Memory Type	DRAM
Maximum Onboard Memory	2MB
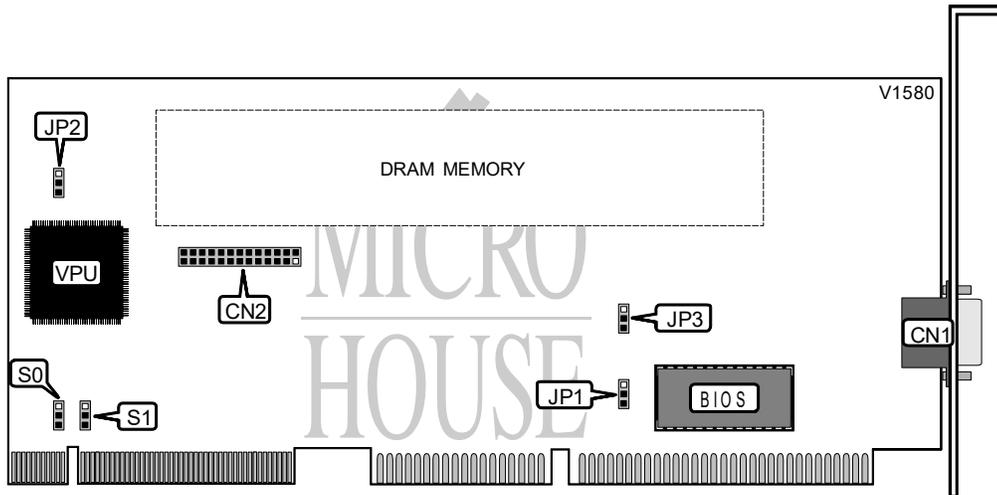
CONNECTIONS			
Purpose	Location	Purpose	Location
15-pin analog video port	CN1	VGA feature connector	CN2

INTERRUPT CONFIGURATION	
IRQ 2/9	JP1
í Disabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

MAIN MEMORY MAP DECODER SELECTION	
Memory	JP2
í 64MB	Pins 1 & 2 closed
128MB	Pins 2 &3 closed

RAMDAC SHADOW CONFIGURATION	
Setting	JP3
í Disabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

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FACTORY CONFIGURED - DO NOT ALTER	
Jumper	Position
S0	N/A
S1	N/A

DRAM CONFIGURATION		
Size	Bank (	Bank ·
1MB	(8) 256K x 4	NONE
2MB	(8) 256K x 4	(8) 256K x 4
Note: DRAM chip size can also be 256K x 8 depending on the Cirrus Logic GD542X version you have. Relative location of Bank 0 & Bank 1 is unidentified by the manufacturer.		